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INSTRUCTIONS

MODEL AVR-A-1-S1-PS-FN PULSE GENERATOR

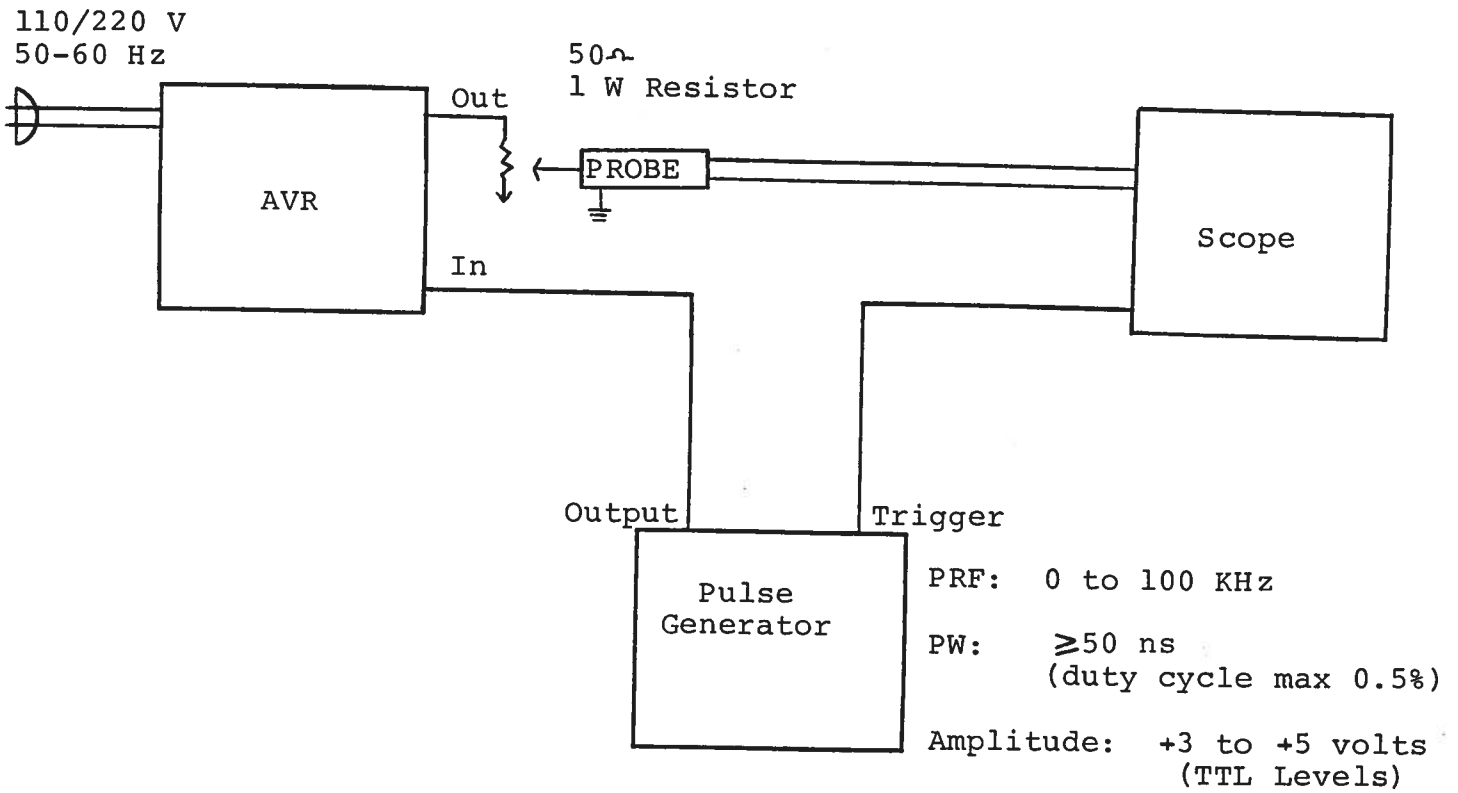
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WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

MODEL AVR-A-1-PS-PN PULSE GENERATOR

TEST ARRANGEMENT



Notes:

- 1) The equipment should be connected in the general fashion shown above. Since the AVR unit provides an output pulse rise time as low as 2 ns, a fast oscilloscope (at least 500 MHz and preferably 2000 MHz) should be used to display the waveform.
- 2) The output PRF is equal to the input trigger pulse PRF.
- 3) WARNING: Model AVR may fail if triggered at a PRF greater than 100 kHz or if the duty cycle exceeds 0.5%.
- 4) The output amplitude is controlled by means of the one turn potentiometer (AMP).
- 5) The output pulse width is controlled by the one turn PW control.
- 6) The desired output polarity is selected by means of the POLARITY switch. With the POLARITY switch in the P position, the negative output pulse generator is rendered inactive. Likewise, with the POLARITY switch in the N position, the positive pulse generator is rendered inactive.
- 7) AVR units with a serial number higher than 5600 are protected by an automatic overload protective circuit which controls the front panel overload light. If the unit is overloaded (by operating at an exceedingly high duty cycle), the protective circuit will turn the output of the instrument OFF and turn the indicator light ON. The light will stay ON (i.e. output OFF) for about 5 seconds after which the instrument will attempt to turn ON (i.e. light OFF) for about 1 second. If the overload condition persists, the instrument will turn OFF again (i.e. light ON) for another 5 seconds. If the overload condition has been removed, the instrument will turn on and resume normal operation. Overload conditions may be removed by:
 - 1) Reducing PRF (i.e. switch to a lower range)
 - 2) Reducing pulse width (i.e. switch to a lower range)

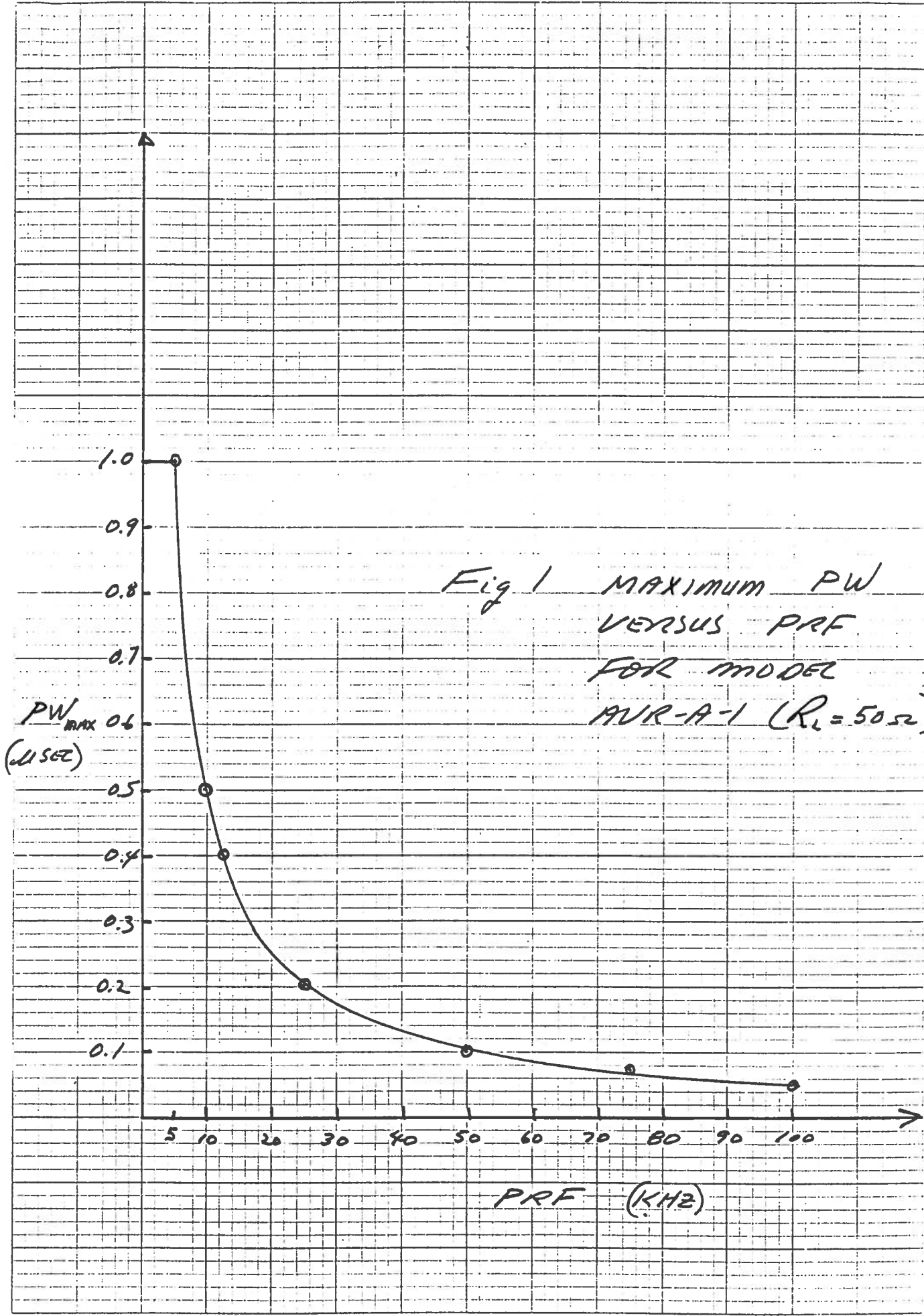
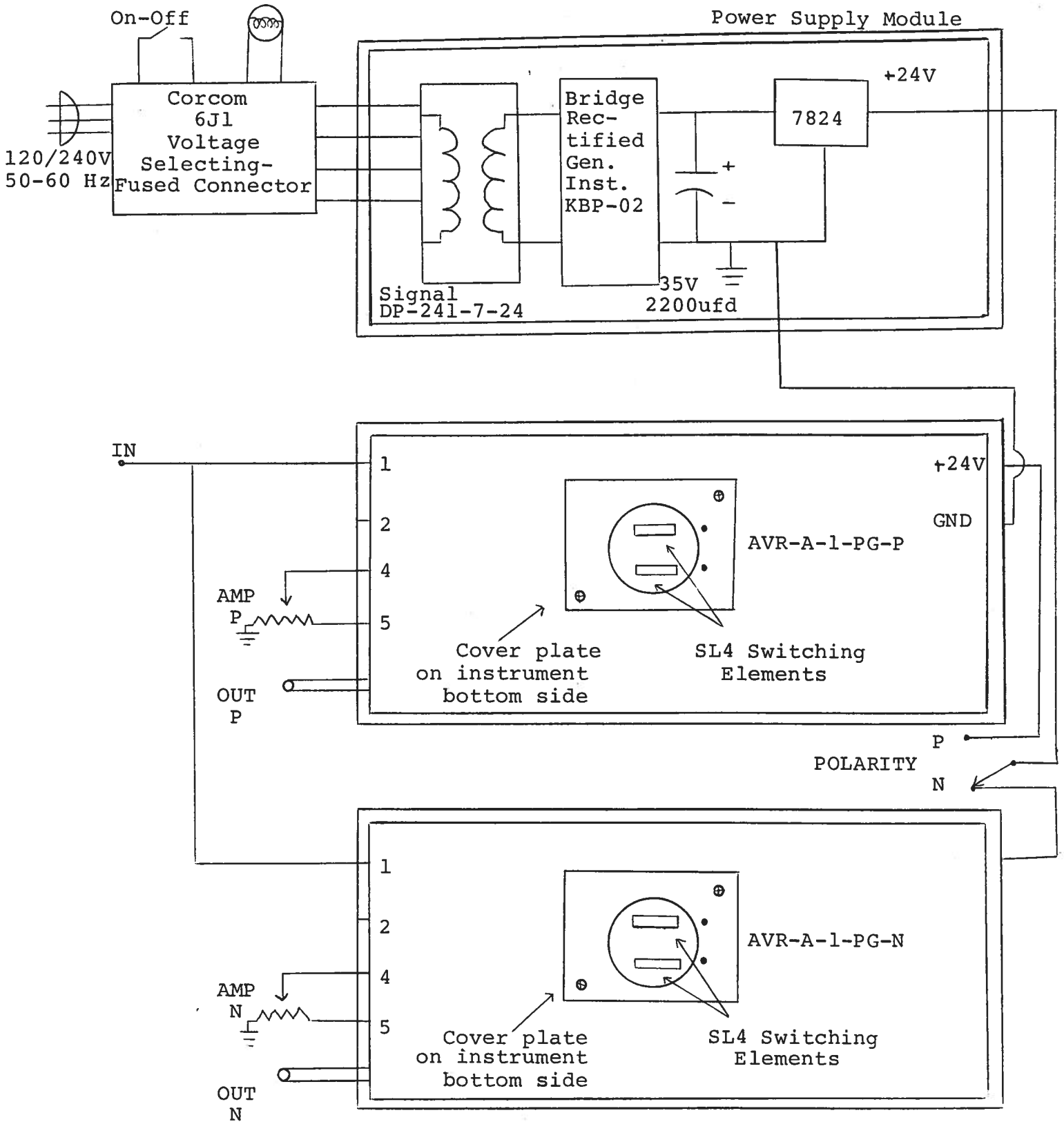


Fig 1 MAXIMUM PW
VERSUS PRF
FOR MODEL
AVR-A-1 ($R_L = 50 \Omega$)

Fig. 2

SYSTEM BLOCK DIAGRAM



SYSTEM DESCRIPTION AND REPAIR PROCEDURE

The AVR consists of two pulse generator modules (POS and NEG) and a power supply board which supplies +24 volts (600 mA max) to the pulse generator module. In the event that the unit malfunctions, remove the instrument cover by removing the four Phillips screws on the back side of the unit. The top lid may then be slid off. Measure the voltage at the +24V pin of the PG module. If this voltage is substantially less than +24 volts, unsolder the line connecting the power supply and PG modules and connect 50 ohm 10 W load to the PS output. The voltage across this load should be about +24V DC. If this voltage is substantially less than 24 volts the PS module is defective and should be repaired or replaced. If the voltage across the resistor is near 24 volts, then the SL4 switching elements in the AVR-PG module have probably failed. The SL4 switching elements are easily replaced by removing the cover plate on the instrument bottom side and extracting the SL4 switching elements from their sockets using a pair of needle nose pliers. Before attempting this first insure that the prime power is off and also briefly ground the metal tabs on the SL4 elements to the chassis as the bypass capacitors may be charged to 225 volts. Replacement SL4 units must be ordered directly from Avtech. When reinstalling the SL4 units in their sockets, insure that the shortest of the three terminals is adjacent to the black dot on the AVR-PG chassis.

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