

# AVTECH ELECTROSYSTEMS LTD.

NANOSECOND WAVEFORM ELECTRONICS  
ENGINEERING . MANUFACTURING

□ P.O. BOX 265  
OGDENSBURG  
NEW YORK  
13669  
(315) 472-5270

BOX 5120 STN. "F"  
OTTAWA, ONTARIO  
CANADA K2C 3H4  
y □ (613) 226-5772  
TELEX 053-4591

## INSTRUCTIONS

MODEL AVR-A-2N-PW-S1-EA PULSE GENERATOR-DRIVER

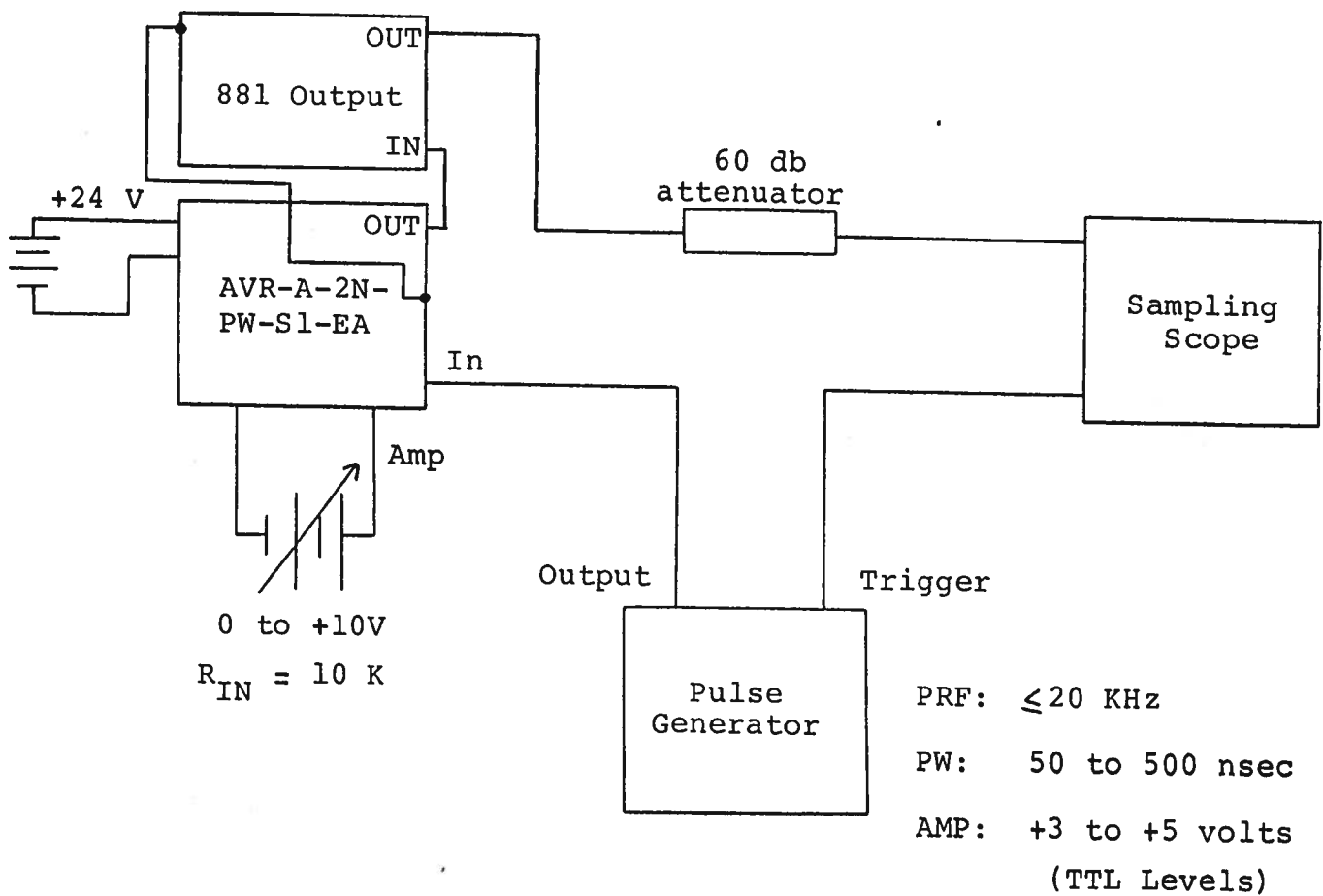
S.N.:

Modified January 1986. 881 output module added to improve rise time and to reduce output droop.

### WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

MODEL AVR-A-2N-PW-S1-EA PULSE GENERATOR TEST ARRANGEMENT



Notes:

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed several GHz.
- 2) The use of a 60 db attenuator will insure a peak input signal to the sampling scope of less than one volt.
- 3) In general, the source pulse generator trigger delay control should be set in the 0.1 to 1.0 usec range. Other settings should be as shown in the above diagram.
- 4) The output pulse width is controlled by means of the one turn potentiometer (PW).
- 5) The output amplitude is controlled by a DC voltage (0 to +10V) applied to the AMP solder terminals (see attached calibration curve).

02.04.86

1. The first part of the document is a list of names and addresses of the members of the committee. The names are listed in alphabetical order and include the following: [illegible names]

2. The second part of the document is a list of the names of the members of the committee who have been elected to the office of Chairman and Vice-Chairman. The names are listed in alphabetical order and include the following: [illegible names]

3. The third part of the document is a list of the names of the members of the committee who have been elected to the office of Secretary and Treasurer. The names are listed in alphabetical order and include the following: [illegible names]

4. The fourth part of the document is a list of the names of the members of the committee who have been elected to the office of Member-at-Large. The names are listed in alphabetical order and include the following: [illegible names]

5. The fifth part of the document is a list of the names of the members of the committee who have been elected to the office of Member-at-Large. The names are listed in alphabetical order and include the following: [illegible names]