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INSTRUCTIONS

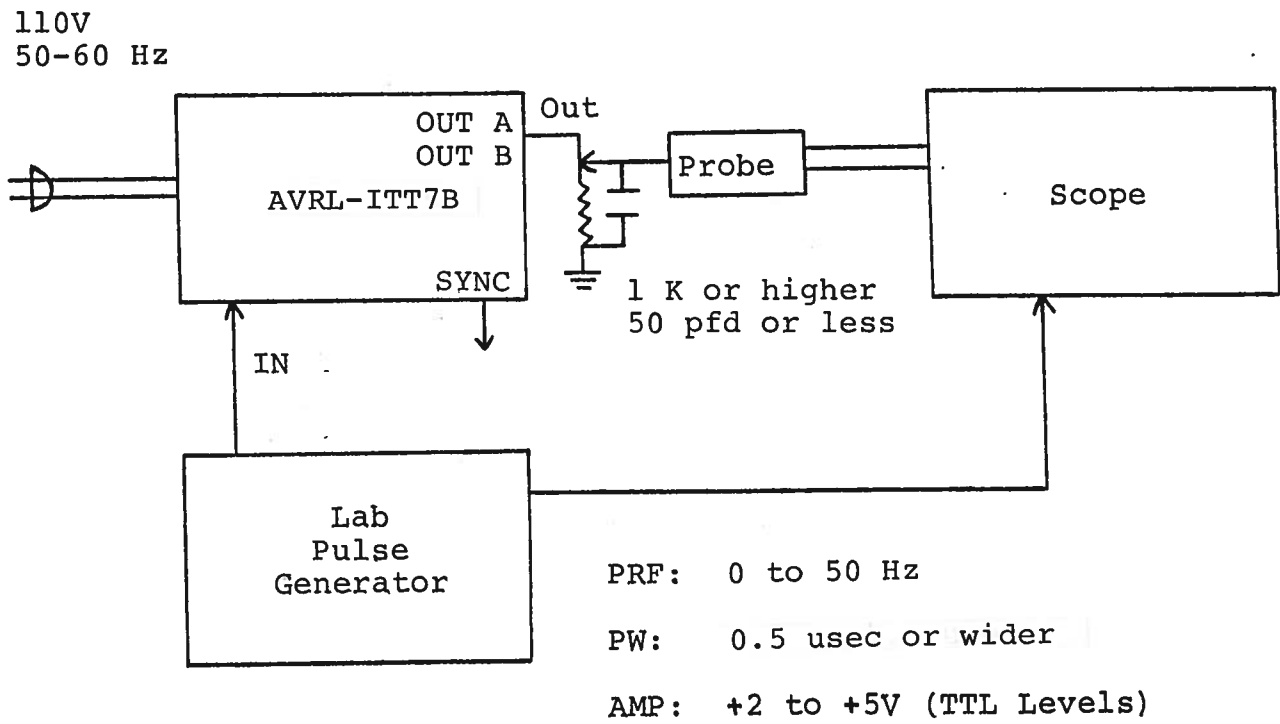
MODEL AVRL-ITT7B PULSE GENERATOR

S.N. :

WARRANTY

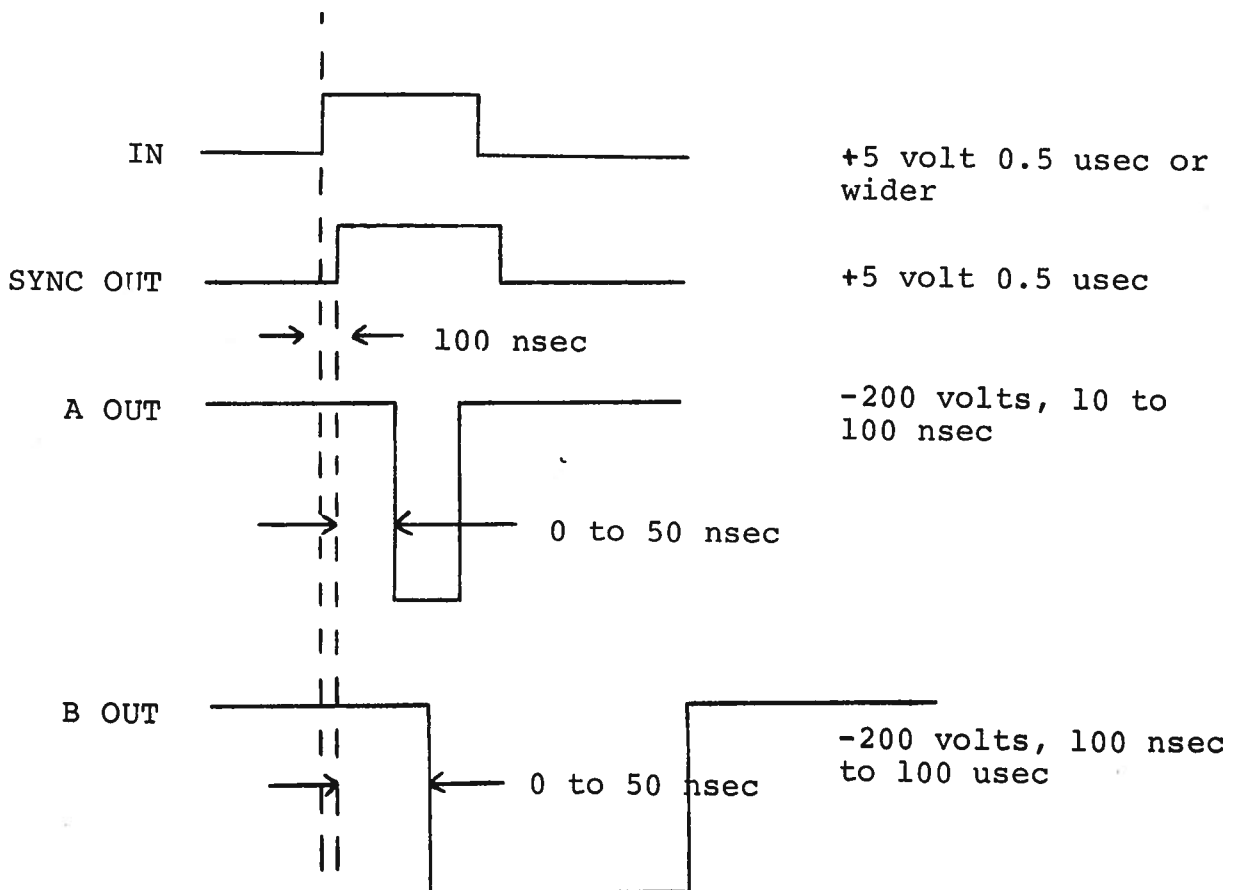
Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

TEST ARRANGEMENT



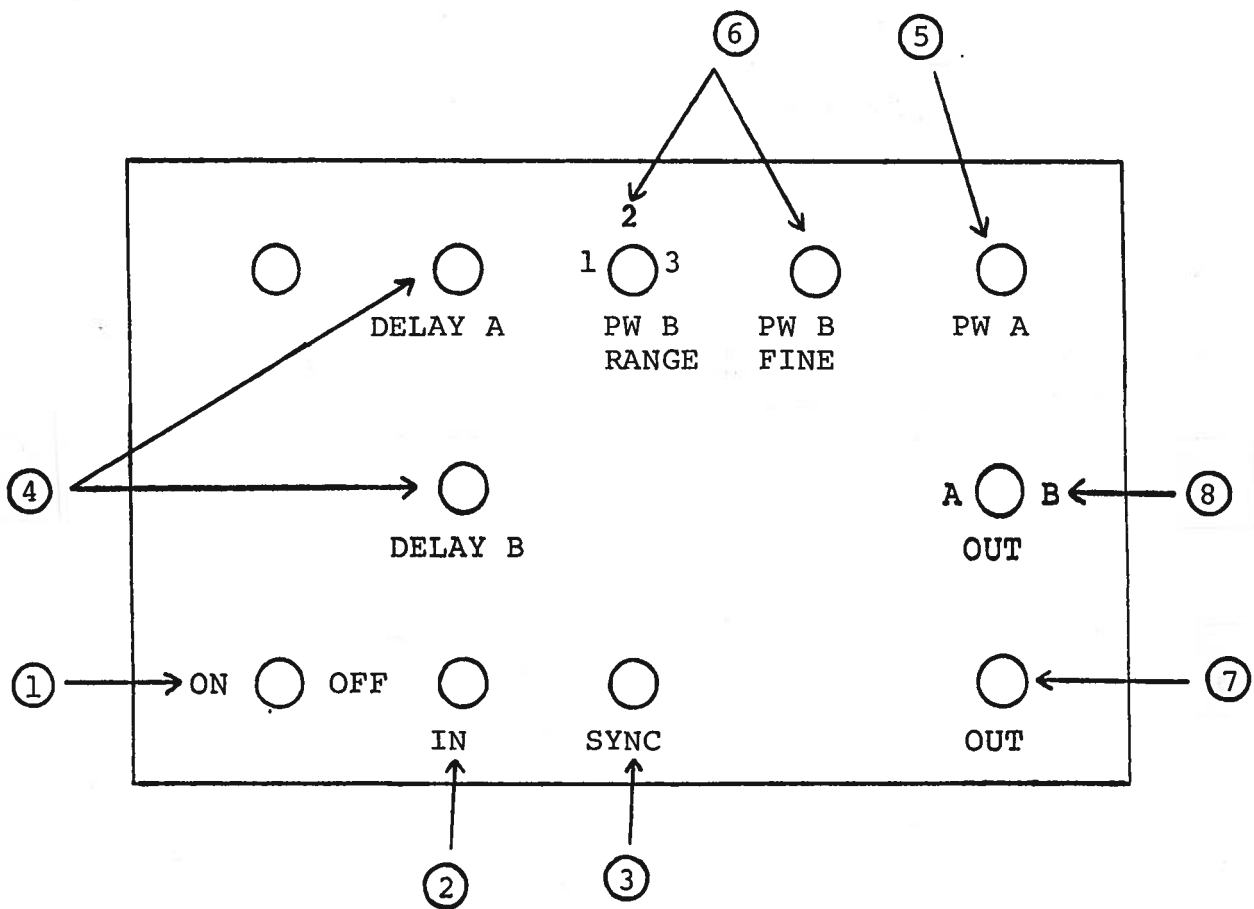
Notes:

- 1) The equipment should be connected in the general fashion shown above. A scope with a bandwidth of at least 50 MHz should be used to view the outputs.
- 2) Both output amplitudes are fixed at -200 V. Care should be taken to insure that the scope and the load resistor can withstand this high voltage (and high output power for wide output pulse widths).
- 3) The output pulse width for output A is variable from 10 nsec to 100 nsec. The output pulse width for output B is variable from 100 nsec to 100 usec.
- 4) The SYNC output provides a 0.5 usec wide TTL level (+3 to +5 volts) pulse capable of driving a 50 ohm (or higher) load. The SYNC output appears 100 nsec after the leading edge of the input trigger pulse. Outputs A and B are delayed with respect to the SYNC output by 0 to 50 nsec depending on the DELAY A and DELAY B setting as shown below:



- 5) The output PRF is equal to the input PRF applied to the IN port. Note that both A and B outputs may fail if above PRF specifications and duty cycle are exceeded or if the load capacitance specification is exceeded.
- 6) Outputs A and B are designed to operate directly into a high impedance load (1 K or higher). WARNING: Unit may fail if operated into a 50 ohm load. The following arrangement is recommended when the unit is used to pulse a biased high impedance load:
- 7) The output switching elements (5L9H) will probably fail if the output of the unit is accidentally short-circuited or if the unit is operated at high output pulse width - high PRF combinations. The switching elements are easily replaced following the instructions given in the REPAIR Section.
- 8) CAUTION: The instrument will be damaged if the load capacitance exceeds 50 pfd or if the load resistance is less than 1 K. Note that coaxial cables connected to the pulse generator output ports typically contribute 15 pfd per foot of length and so this capacitance must be included in the total.

FRONT PANEL CONTROLS



- (1) ON-OFF Switch. Applies prime power to all stages.
- (2) IN. Input trigger for A and B outputs and SYNC output applied here (TTL levels, 0.5 usec or wider).
- (3) SYNC. TTL level (+3 to +5 volt) 0.5 usec pulse which appears 100 nsec after the leading edge of the IN pulse. Will drive 50 ohms or higher.
- (4) DELAY A, DELAY B. Varies propagation delay from SYNC to A OUT, B OUT as follows:

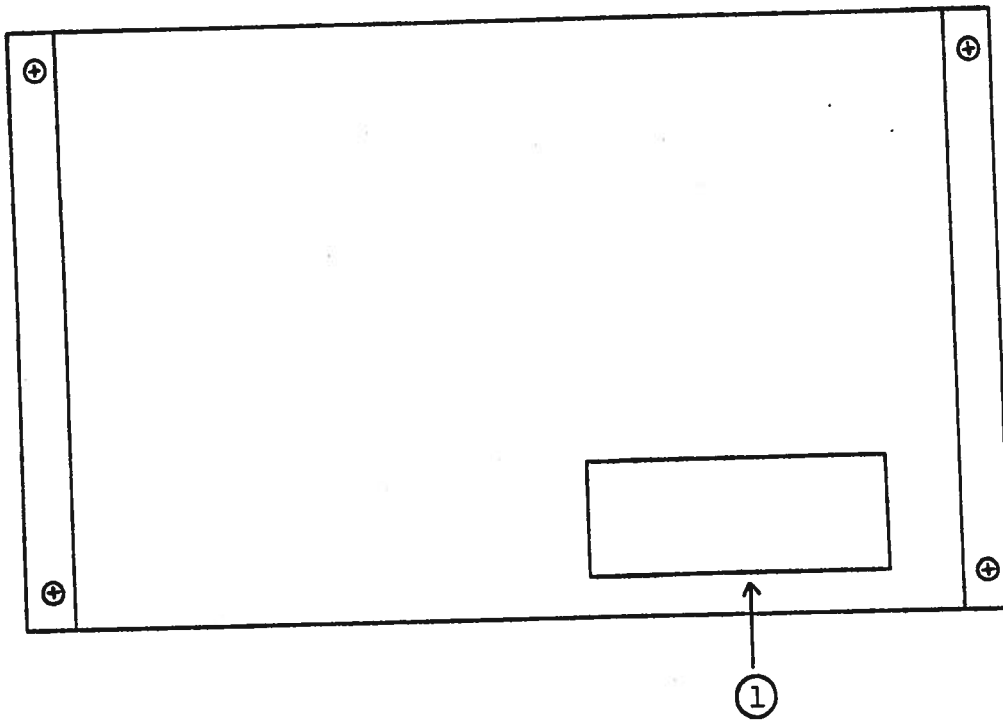
A OUT	B OUT
0 to 50 nsec	0 to 50 nsec

- (5) PW A. One turn control used to vary output PW (A) from 10 to 100 nsec.
- (6) PW B. One turn control and 3 position range switch used to vary pulse width of output B from 100 nsec to 100 usec as follows:

- RANGE 1: 100 nsec to 1.3 usec
- RANGE 2: 1.2 usec to 16 usec
- RANGE 3: 12 usec to 116 usec

- (7) OUT Connector. BNC connectors used to connect output of A and output of B to high impedance load.
- (8) A-B OUTPUT Switch. With switch in the A position, pulse generator A is connected to the output connector (B) and the B pulse generator is disabled. With switch in the B position, pulse generator B is connected to the output connector (B) and the A pulse generator is disabled.

BACK PANEL CONTROLS



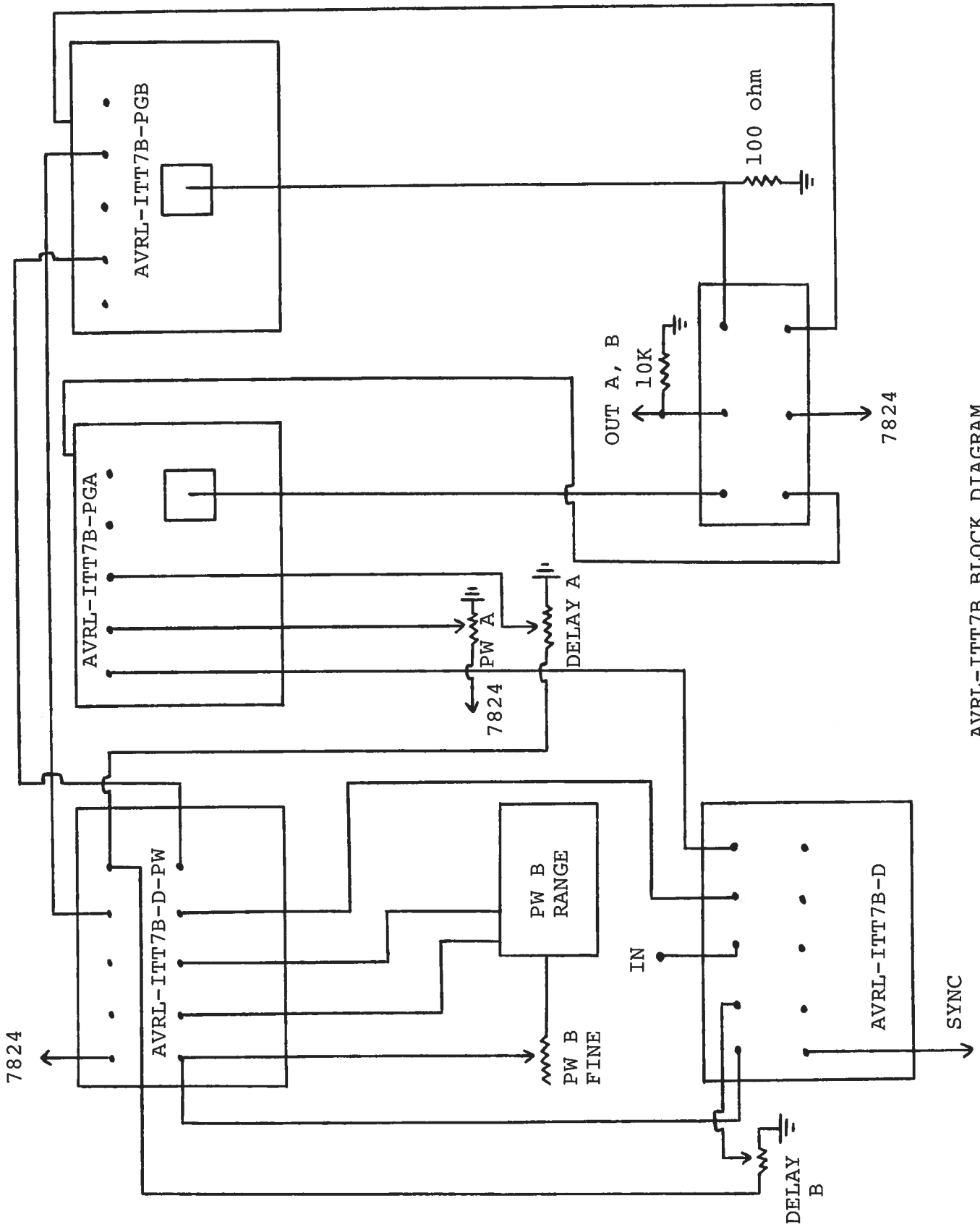
- (1) FUSED CONNECTOR, VOLTAGE SELECTOR. The detachable power cord is connected at this point. In addition, the removable cord is adjusted to select the desired input operating voltage. The unit also contains the main power fuse.

REPAIR PROCEDURE

- 1) **WARNING:** Before attempting any repairs, note that potentials as high as 220 volts are employed in the chassis structure.
- 2) The pulse generator is constructed from the following subsystems or modules:
 - a) Metal chassis
 - b) A pulse generator module (AVRL-ITT7B-PGA)
 - c) B pulse generator module (AVRL-ITT7B-PGB)
 - d) Delay module (AVRL-ITT7B-D)
 - e) +24V power supply board
 - f) B pulse generator pulse width module (AVRL-ITT7B-PW-D)

The modules are interconnected as shown in the following diagram.

- 3) If no output is provided by the A or B outputs then it is most likely that the SL9H switching elements in the output stages have been damaged and should be replaced using the following procedure:
 - i) Turn off prime power and remove cover plate on bottom of instrument (four 2-56 screws).
 - ii) By means of a screwdriver, briefly ground the tabs of the two SL9H transistors to discharge the bypass capacitors.
 - iii) Extract the old SL9H transistors from their socket by means of needle-nosed pliers.
 - iv) Install replacement SL9H transistors and install cover plate.

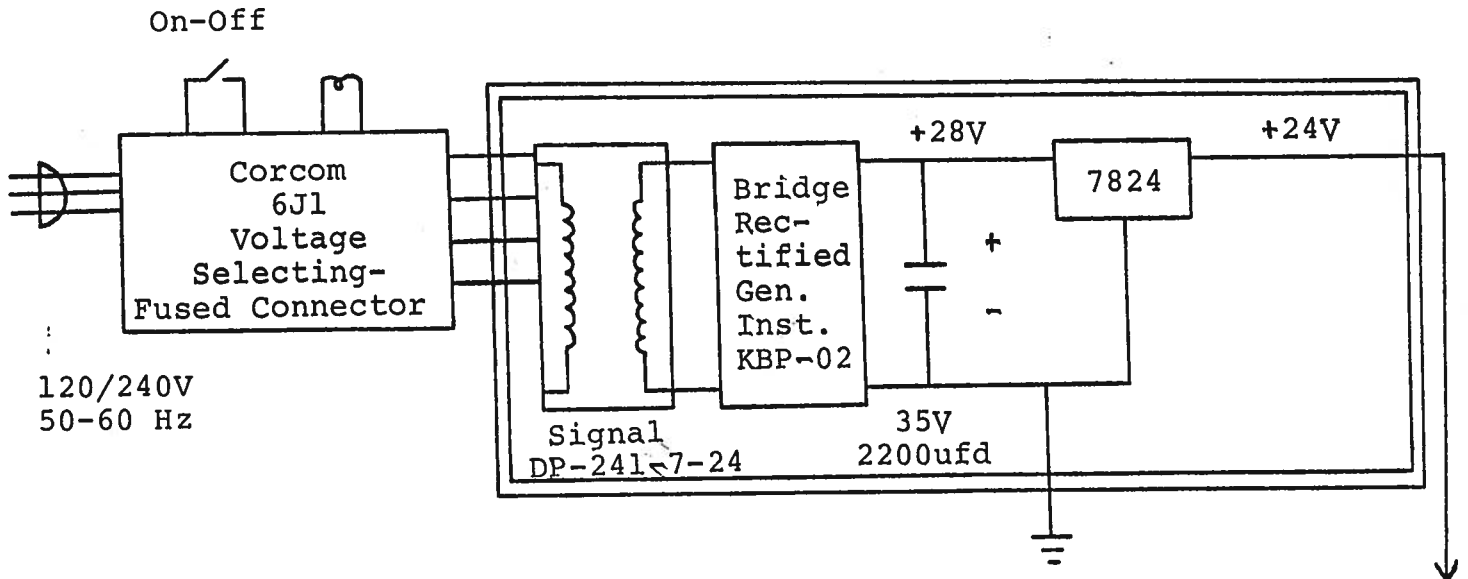


AVRL-ITT7B BLOCK DIAGRAM

+24 VOLT POWER SUPPLY

The AVRL-IIT7B consists of the five standard modules and a power supply board which supplies +24 volts (600 mA max) to the modules. In the event that the unit malfunctions, remove the instrument top cover, thereby exposing the modules. Measure the voltage at the +24 V pin of the PS module. If this voltage is substantially less than +24 volts, unsolder the line connecting the power supply board output and connect a 50 ohm 10 W load to the power supply output. The voltage across this load should be about +24 V DC. If this voltage is substantially less than 24 volts the power supply board is defective and should be repaired or replaced. If the voltage is near +24V then see instructions in preceding section.

POWER SUPPLY BOARD



Schroff

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