## AVTECH ELECTROSYSTEMS LTD.

NANOSECOND WAVEFORM ELECTRONICS ENGINEERING - MANUFACTURING (315)472-5270

## INSTRUCTIONS

MODEL AVX-D-BAS4A-ED DELAY GENERATOR

WARFANTY
Avtech Electrosystems Ltd. warrants products of its
manufacture to be free from defects in material and
workmanship under conditions of normal use. If, within one
year after delivery to the original owner, and after prepaid
return by the original owner, this Avtech product is found to
be defective, Avtech shall at its option repair or replace
said defective item. This warranty does not apply to units
which have been dissembleds modified or subjected to
conditions exceeding the applicable specifications or
ratings. This warranty is the extent of the obligation or
$l i a b i l i t y ~ a s s u m e d ~ b y ~ A v t e c h ~ w i t h ~ r e s p e c t ~ t o ~ t h i s ~ p r o d u c t ~ a n d ~$
no other warranty or guarantee is either expressed or implied.


BACK PANEL CONTROL


1) Delay control connector:

Amphenal 57-40500
Pin connections:

Chamnel

| 1 | 1 |  | 8 |
| :---: | ---: | ---: | ---: |
| 2 | 9 |  | 16 |
| 3 | 17 |  | 24 |
| 4 | 26 |  | 33 |
| 5 | 34 |  | 41 |
| 6 | 42 |  | 49 |
| GND |  | 50 |  |

Logic Level
$\begin{array}{ll}0 & 0 \text { to +0. 日V } \\ 1 & +2 \text { to }+5 V\end{array}$
2) Latch control connector:

Amphenol 57-40140

Pin cannections:

Channel Pin

| 1 | 1 |
| :--- | :--- |
| 2 | 2 |
| 3 | 3 |
| 4 | 4 |
| 5 | 5 |
| 6 | 6 |

Logic Level

```
O (O VDC) - & bit digital code is active
1 (+5 VDC) - freezes digital code at cor-
    responding g bit input
```

3) Delay equalization pots:

The minimum delays for the six channels may be equalized (at approx. 20 nsec) by minor adjustments to the one turn pots. Clockwise rotation of the pots increases the propagation delay ( $\pm 100$ psec adjustment range). The propagation delays were matched to within $\pm 10$ psec prior to shipment.
4) Corcom connector:

Detachable line cord comnection. Also contains line voltage switching card (120-240 volts) and line fuse (0.5A 5B).
2) The minimum delay between the input and output is about 20 nsec. The minimum delay for the six channels may be equalized by means of the six rear panel one turn pots (approx. $\pm 100$ psec). The delays were matched to within $\pm 10$ psec prior to shipping.
3) 日 bit control words applied to the rear panel 50 Pin D connector vary the propagation delay in 10 psec steps up to 2.56 nsec. The 10 psec step size may be varied by very minor adjustment to the si\% one turn pots on the AVX-D-BAS4A modules in the instrument interior. To access the pots remove the 4 Fhillips screws on the instrument back panel and then remove the instrument top cover. The step size was set to 10 psec prior to shipping.
4) With 0 volts applied to the latch control the relative delay through the channel is controlled by the $日$ bit contral mode. With 5 valts applied ta the latch contral; the relative delay is frazen at the mode determined by the last $\theta$ bit word.
5) The stability of the propagation delay and the step size was checked using the test arrangement shown in Fig. 1 .


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