# AVTECH ELECTROSYSTEMS LTD. <br> NANOSECOND WAVEFORM ELECTRONICS ENGINEERING - MANUFACTURING 

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## INSTRUCTIONS

MODEL AVX-D-EAS4A-ED DELAY GENERATOR

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## WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been dissembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.


## BACK PANEL CONTROL


1)

Logic Level
$0 \quad 0$ ta +0.8V
1

MSE

8 16 24 3.3 41 49
50

| 1 | 8 |
| ---: | ---: |
| 9 | 16 |
| 17 | 24 |
| 26 | 33 |
| 34 | 41 |
| 42 | 49 |

> LSE

Valts

$$
\begin{array}{lll}
0 \text { to }+0.8 V & (<5 u A) & \text { Note that the } \\
+2 \text { to }+5 v & (<5 \mathrm{uA}) & \text { potential at } \\
& & \\
& & \text { Fpen-circuited } \\
& & -4.0 \text { volts. }
\end{array}
$$

Delay equalization pots:
The minimum delays for the six channels may be equalized (at approx= 20 nsec) by minor adjustments to the one turn pots. Clockwise rotation of the pots increases the propagation delay $\{ \pm 100$ psec adjustment range). The propagation delays were matched to within $\pm 10$ psec prior to shipment.

Corcom connector:
Detachable line cord connection. Also contains line voltage switching card (120-240 volts) and line fuse (0.5A 5B).

Channel ON-DFF Switches:
These switches were disabled during the March 1990 modifications. All six channels are permanently on.

1) The unit requires a warm-up time of at least 30 minutes. The rear panel DN-OFF switch must be ON during the warmup period.
2) The minimum delay between the input and output is about 20 nsec. The minimum delay for the six channels may be equalized by means of the six pear panel one turn pots (approx. $\pm 100$ psec). The delays were matched to within $\pm 10$ psec priar to shipping.
3) 8 bit control wards applied to the rear panel 50 Fin D connector vary the propagation delay in 10 psec steps up to 2.56 nsec. The 10 psec step size may be varied by very minor adjustment to the six one turn pots on the AVX-D-BAS4A modules in the instrument interiar. To access the pots remove the 4 Fhillips screws on the instrument back panel and then pemove the instrument top cover. The step size was set to 10 psec prior to shipping.
4) The stability of the propagation delay and the step size was checked using the test arrangement shown in Fig. 1.


03.21 .90 (edition B)

[^0]:    S.N.:

