



AVTECH ELECTROSYSTEMS LTD.

NANOSECOND WAVEFORM ELECTRONICS
SINCE 1975

□ P.O. BOX 265
OGDENSBURG, NY
U.S.A. 13669-0265
TEL: (315) 472-5270
FAX: (613) 226-2802

TEL: 1-800-265-6681
FAX: 1-800-561-1970
U.S.A. & CANADA

☒ BOX 5120 STN. F
OTTAWA, ONTARIO
CANADA K2C 3H4
TEL: (613) 226-5772
FAX: (613) 226-2802

INSTRUCTIONS

MODEL AVX-D-2 DELAY GENERATOR

S.N. :

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

SPECIFICATIONS

MODEL AVX-D-2

Model designation: AVX-D-2

Delay range: 100 nsec to 10 usec
Low range: 90 nsec to approx.
700 nsec
Med. range: 650 nsec to 2.90 usec
High range: 2.90 usec to 10 usec
10 turn DL pot control determines
delay within each range

PRF range: 0 to 1 MHz
Note that at given PRF, max delay
can not exceed one half of period
of PRF

Jitter: ± 30 psec at min DELAY
 ± 60 psec at max DELAY (confirmed
using AVH-S-1 130 psec impulse
generators & sampling scope display)

Input PW: 50 to 500 nsec

Output PW: 200 to 300 nsec

Trig. PW: Equals input PW

Prop delay, IN to
TRIG port: ≤ 10 nsec

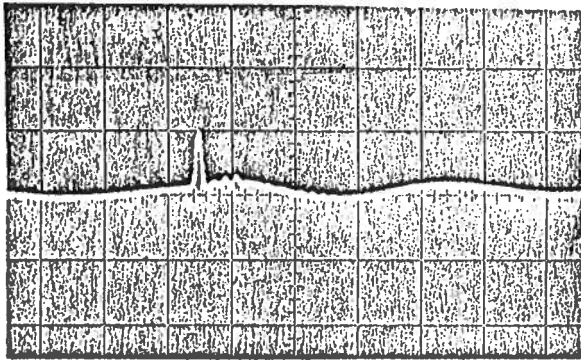
Signal amplitudes: TTL levels
Outputs will drive 50 ohm loads

Power requirements: +15V DC 100 mA

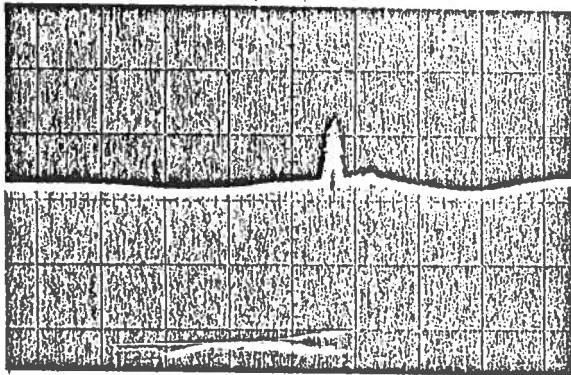
Connectors: SMA

Chassis: As AVX-D but with addition of
range switch

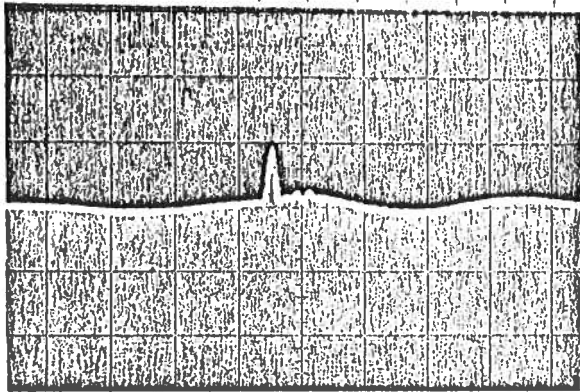
SITTER TEST RESULTS



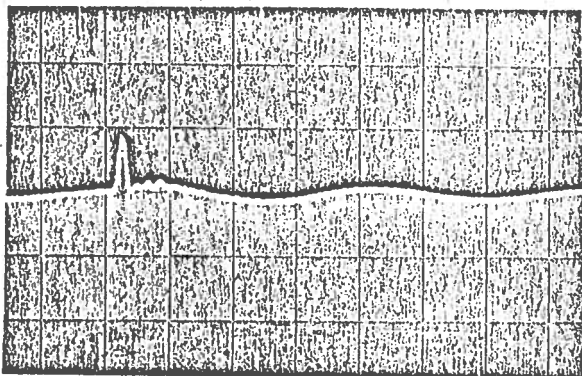
a) AMH-5-1 OUTPUT
NO DELAY,
ie NO AMX-D-2
UNITS



b) 9 μ sec
DELAY



c) 2 μ sec
DELAY

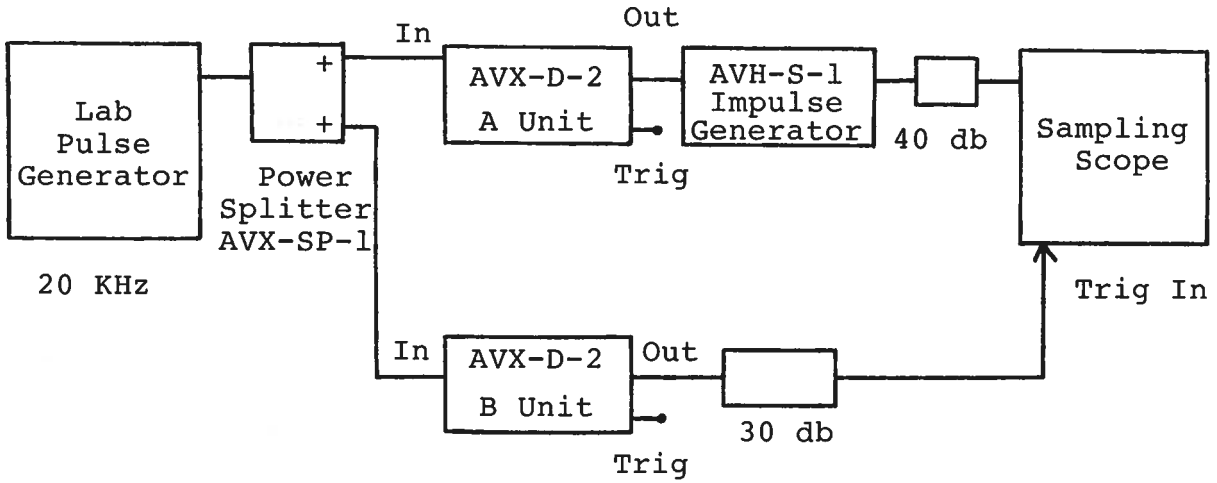


d) 0.4 μ sec
DELAY

ALL PHOTOS
1.0 μ sec / DIV
10 VOLTS / DIV
20 KHz.

MODEL AVX-D-2

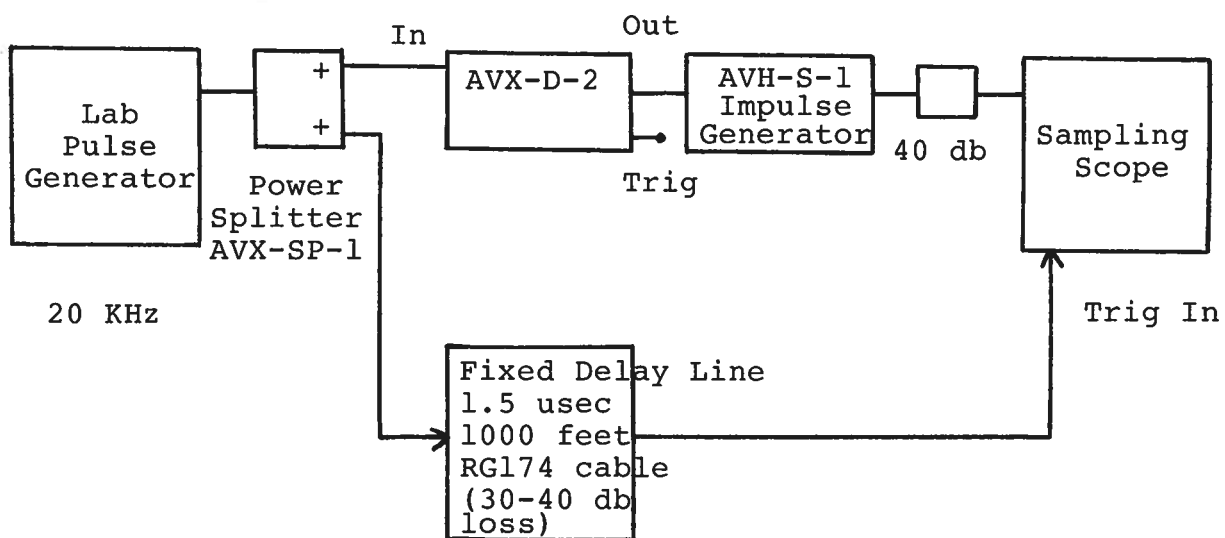
JITTER AND DELAY TEST ARRANGEMENT



- 1) A worse-case jitter test of the AVX-D-2 unit was conducted using the above arrangement. The use of two delay generators serves to:
 - a) Aggravate the jitter since the resultant jitter is the consequence of two units.
 - b) Allow triggering of the sampling scope with long delays (eg. 10 usec) on the AVX-D-2 units.
- 2) The delays of the A and B units should be such that the time base of the sampling scope is triggered slightly ahead (eg. 20-100 nsec) of the application of the impulse to the vertical amplifier.
- 3) The photos on the following sheet illustrates the AVH-S-1 output waveform for the following three cases:
 - a) No delay, ie. AVX-D-2 A and B units removed.
 - b) Delay of about 9 usec (High range).
 - c) Delay of about 2 usec (Med. range).
 - d) Delay of about 0.4 usec (Low range).
- 4) The AVX-D-2 delay can be calibrated by means of a real time scope monitoring the time delay between the IN (or TRIG) and OUT ports.

MODEL AVX-D-2

LONG TERM DRIFT TEST ARRANGEMENT



Following a warm-up period of 5 minutes, the AVX-D-2 delay is constant within ± 2 nsec.

