## AVTECH ELECTROSYSTEMS LTD.

NANOSECOND WAVEFORM ELECTRONICS ENGINEERING - MANUFACTURING

## INSTRUCTIONS

MODEL AVX-S1-MI-EGG1-TO-5 EIAS TEE
5. N. :

## WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been dissembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

Fig. 1 BIAS INSERTION UNIT TEST ARRANGEMENT


Fig. 2 FUNCTIONAL EQUIVALENT CIRCUIT


Notes:

1) The DC terminal of the bias insertion must either be shorted to ground (if a DC offset is not required) or a DC power supply must be applied. The 1 aser diode will not function if the DC terminal is open circuited. Note that the DC current must not exceed $\pm 100 \mathrm{~mA}$.
2) The MI port must be terminated inta 50 ohms. Note that a 50 db (or larger) attenuator should be placed between the AVX-S MI output and the scope input since the MI output is very nearly equal to 0.1 the amplitude of the input drive pulse (therefore MI provides outputs as high as 35 volts).
J) The diode current $I_{\text {s }}$ (Amps) and the MI output voltage are related as follows:

$$
I_{D}=0.2 \mathrm{MI}
$$


11.28 .90

