



**AVTECH ELECTROSYSTEMS LTD.**

NANOSECOND WAVEFORM ELECTRONICS  
SINCE 1975

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**INSTRUCTIONS**

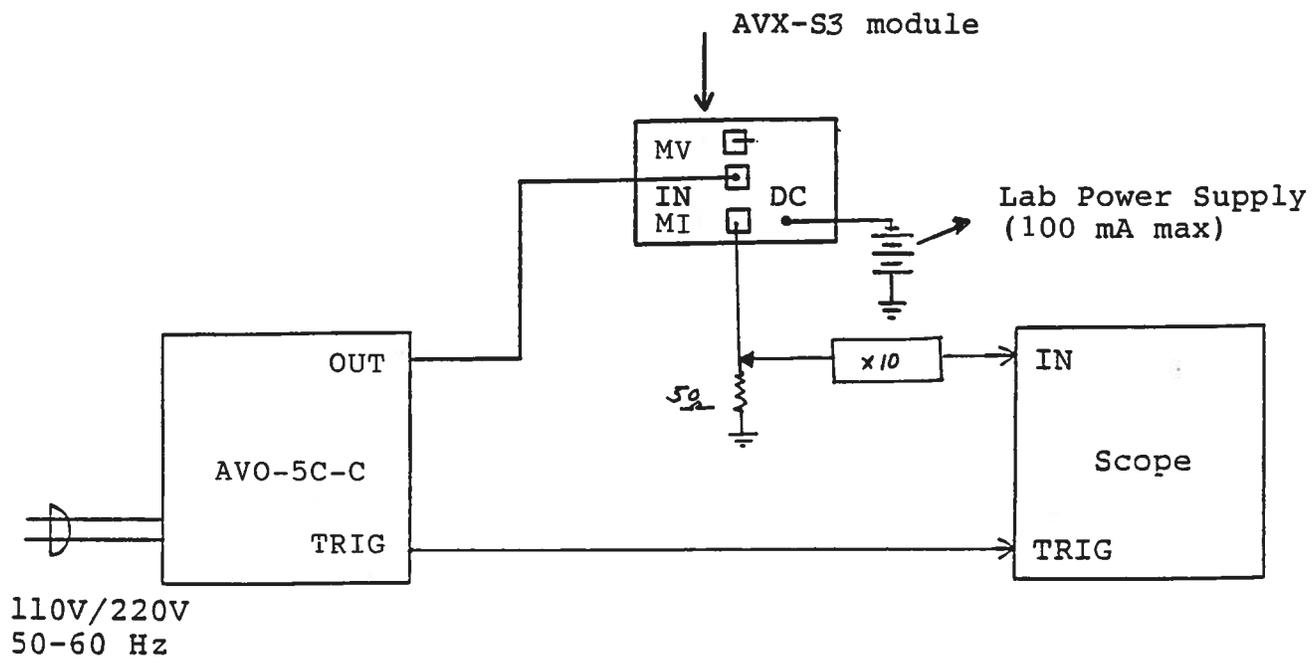
**MODEL AVX-S3A-MV-MI-PA PULSE GENERATOR**

**S.N.:**

### WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation or liability assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

PULSE GENERATOR TEST ARRANGEMENT



### GENERAL INSTRUCTIONS

- (1) The AVX-S1 module should be connected to the AVO-5C-C mainframe via the supplied 24" RG174 cables (two).
- (2) Gently insert the H1 package into the 8 pin socket assembly.
- (3) Connect the MI port to a scope set to 1 Volt/div and with a 50 Ohm load impedance.
- (4) The PINS on the D connector (AMP No. 57-60140) correspond to the PINS on the H1 package with the exception that PINS 4 (cathode) and 5 (anode) are not connected.
- (5) Connect a negative current source to the DC terminal. A lab power supply operating in the current limiting mode is recommended. The source must have a compliance voltage of at least -3 Volts. Slowly increase the offset current to -100 mA (as indicated by the meter on the lab power supply). To avoid transients when connecting the lab power supply, the voltage control knob should be fully CCW and the prime power should be ON. Slowly and cautiously increase the voltage and monitor the current control knob (and ammeter) to insure that the offset does not exceed 100 mA. Final control should be via the current knob. Note that if a DC bias is not applied, the DC in terminal must then be shorted to ground.
- (6) The diode pulse current (Amps) and the Voltage at  $M_1$  (Volts) are related as follows:

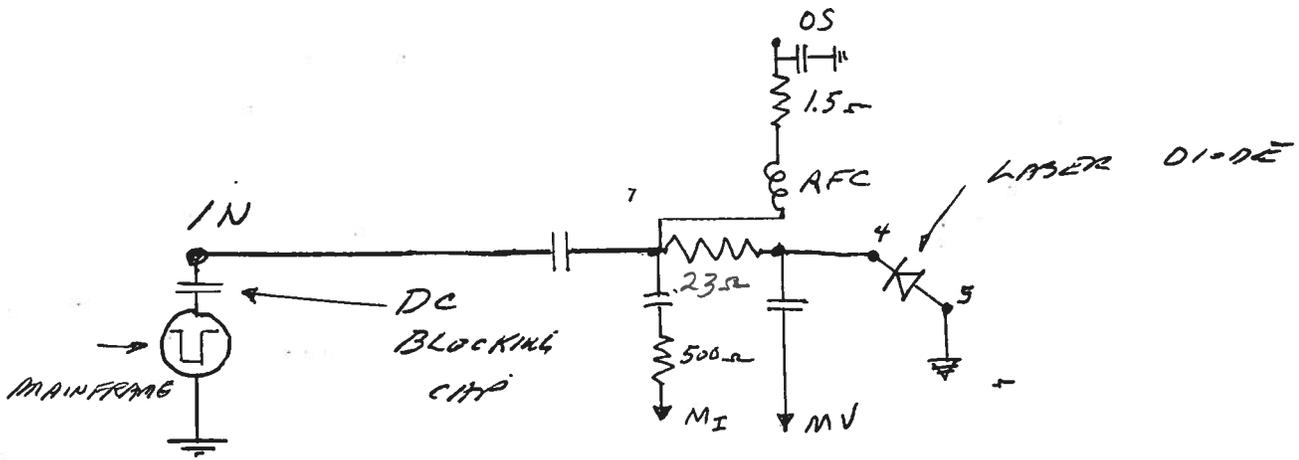
$$I_D = 0.5 V_{M_1}$$

- (7) For additional information:

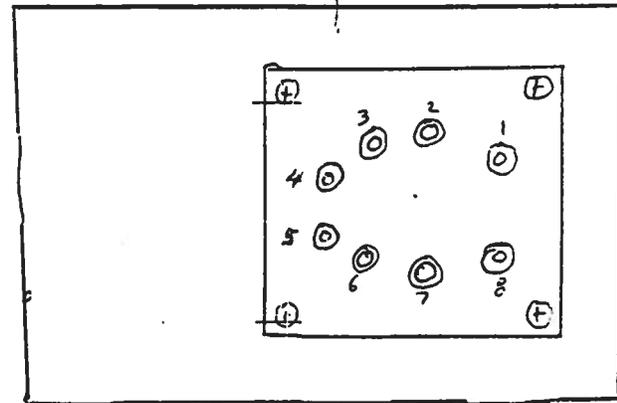
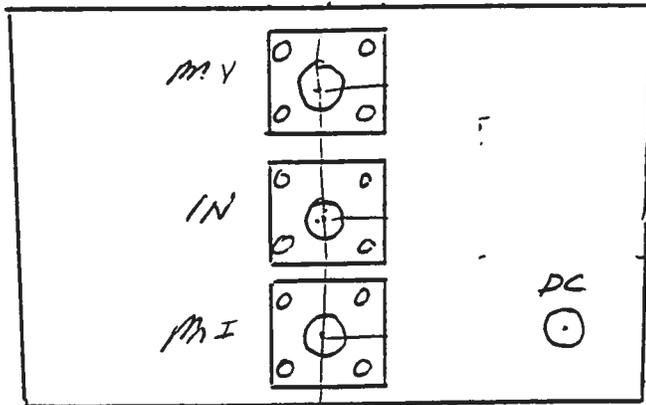
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AVX-51 (SN 8166)



FUNCTIONAL EQUIV. CCT.



PACK THE



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Fax Ref No: 1602 From: Avtech Electrosystems Ltd.

To: Phasemetrics, CA Our Fax No: (613) 226-2802

Date: June 6, 1997

Attn: Carlos Duran Receivers Fax No: 619-646-4455  
Tel: 619-646-4842

Subject: Quotation No. of pages: 1

Following our telephone conversation of June 5th, I am pleased to provide the following price and delivery quotations:

- A) Model designation: AVO-5C-C-N  
Price: \$3,351.00 US each, FOB destination  
Delivery: 60 days ARO
- B) Model designation: AVX-S3A-MV-MI-PA  
Price: \$990.00 US each, FOB destination  
Delivery: 60 days ARO

Model AVX-S3A-MV-MI-PA accepts the SDL 7432-H1 diode and provides a peak current of 2.0 Amps when driven by the AVO-5C-C mainframe. The 7432-H1 plugs into a socket on the face of the module. All PINS (except the laser anode and cathode) are connected to a 14 PIN D connector on the side of the module.

Thank you for your interest in our products. Please call me again (1-800-265-6681) if you require any additional information.

Regards

Dr. Walter Chudobiak  
Chief Engineer

WC:pr

September 9 1971

Dist: AVX-5

File: S3AMVMI.INS