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BOX 5120, LCD MERIVALE  
OTTAWA, CANADA K2C3H5

PERFORMANCE CHECKSHEET

Model: AVX-T  
Type: Ultra-High-Speed Bias Tee  
S.N.: 14448  
Date: May 10, 2024

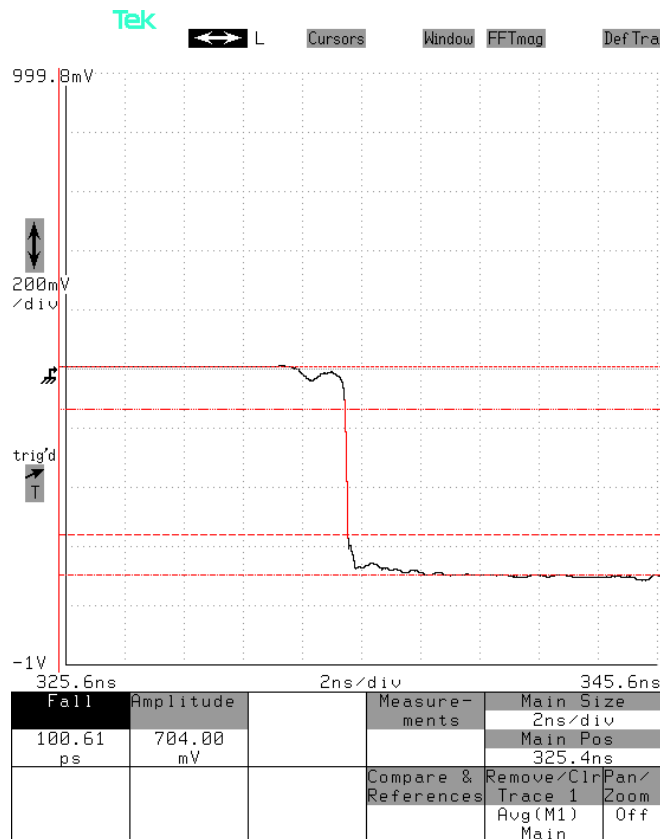
Basic specifications -

Pulse/DC voltage limit: 200V  
DC current limit: 0.5A  
Pulse width range: 0.2 – 50 ns  
Rise time (20%-80%): < 60 ps

Rise Time Test

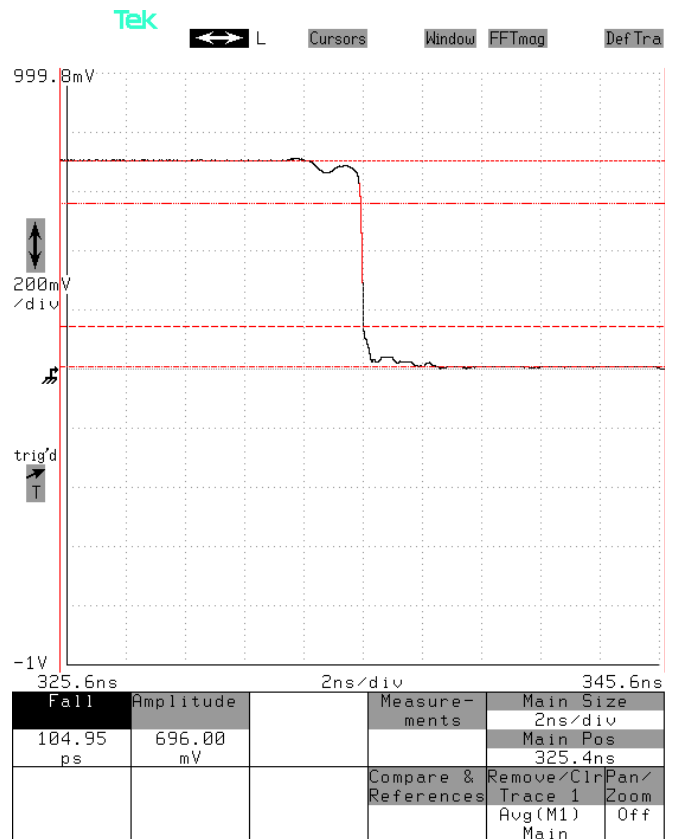
100.61 ps rise time, -7V input to AVX-T,

1 ns/div. 2 V/div (200 mV/div × 20 dB):



Corresponding output of AVX-T with +7V DC bias added,

1 ns/div. 2 V/div (200 mV/div × 20 dB):

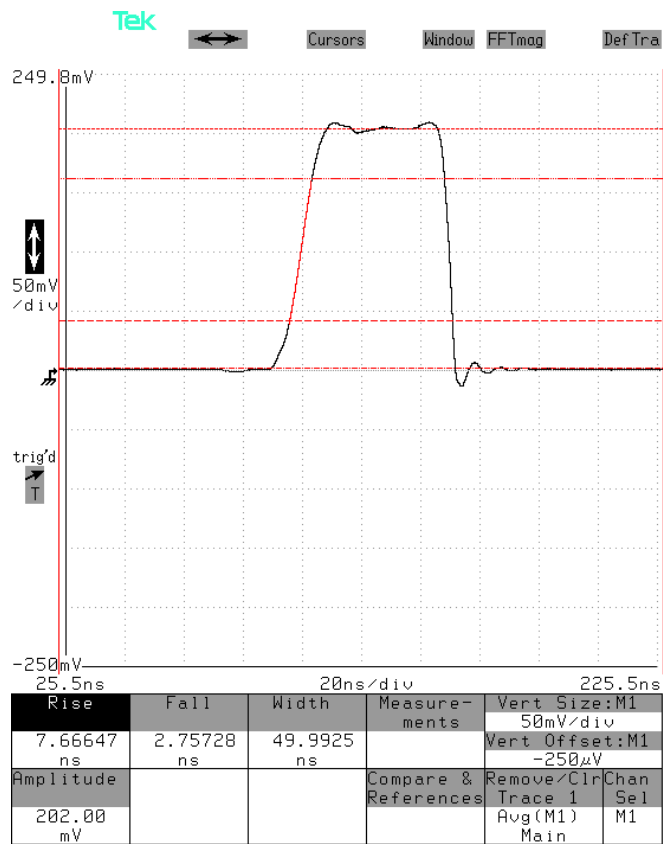


The output rise time should be less than  $\sqrt{((100.61\text{ps})^2 + (60\text{ps})^2)} = 117.1 \text{ ps}$ . The measured rise time does satisfy this limit.

## Maximum Amplitude / PW / Offset Test

200V. 50 ns input to AVX-T, generated by an Avtech AVR-3HE-B pulser

20 ns/div. 50 V/div (50 mV/div × 60 dB):



Corresponding output of AVX-T with +25V DC bias added,

20 ns/div. 50 V/div (50 mV/div × 60 dB):

